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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keiichiro KATA & Shinichi CHIKAKI

Title: PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR WAFER

Serial No.: 10/645,782

Filed: August 22, 2003

Examiner: Mark Prenty

Art Unit: 2822

AMENDMENT AND REPLY UNDER 37 C.F.R. 1.173

Box Amendment
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Amendments to the Claims begin on page 2 of this document.

Remarks/Arguments begin on page 6 of this document.

Please amend the application as follows: